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## ABSTRACT OF THE DISCLOSURE

The invention relates to a low consumption voltage amplifier comprising a transistor (M1), a first current generator (I1) that supplies power to the drain of the transistor (M1), a second current generator (I0) that charges the source of the transistor (M1), the current output by the second current generator (I1) being substantially equal to the value of the current output by the first current generator (I0), a first capacitor (C1) connected to the drain of the transistor (M1) and a second capacitor (C0) connected to the source of the transistor (M1), an additional field effect transistor (M3) of the type opposite to the type of the first field effect transistor being inserted between the current generator (I1) and the first field effect transistor (M1).

FIGURE 7.